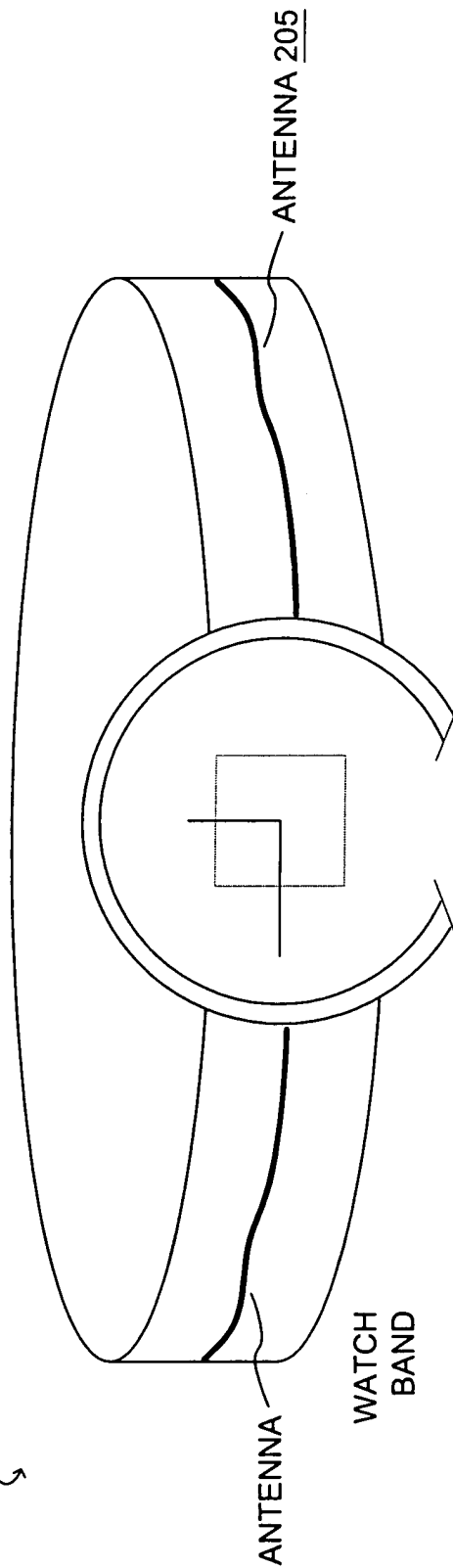


**Fig. 1**

FIG. 2

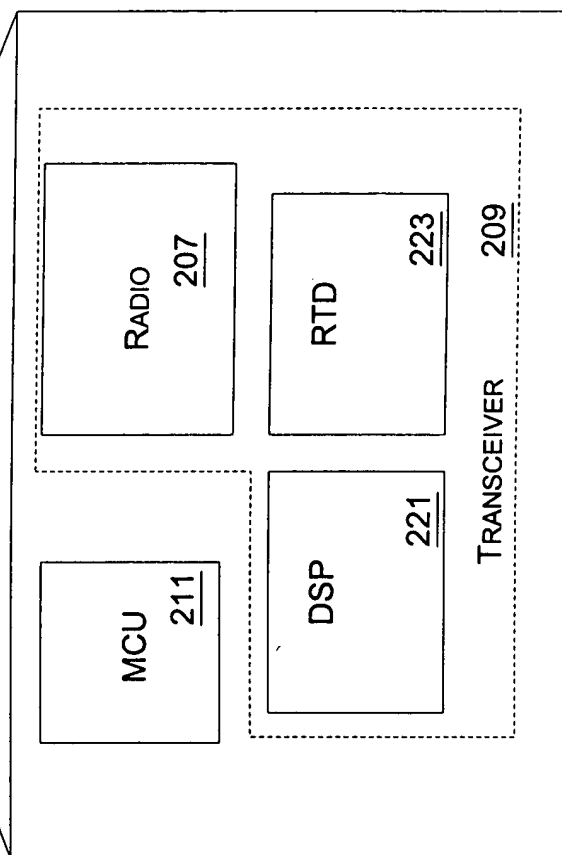
101



WATCH  
BAND

ANTENNA

ANTENNA 205



MCU  
211

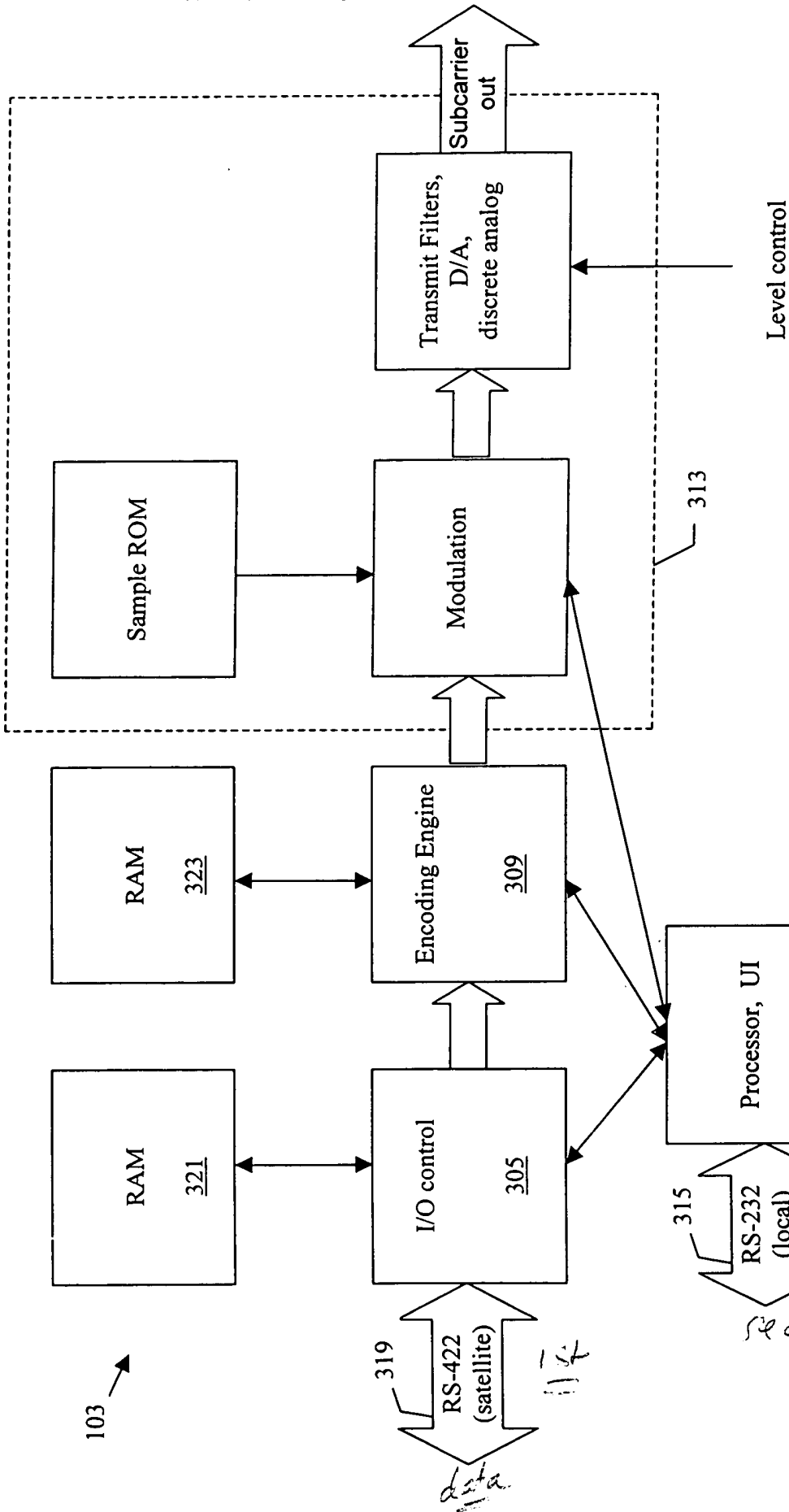
RADIO  
207

DSP  
221

RTD  
223

TRANSCIVER  
209

Fig. 2



**Fig. 3**

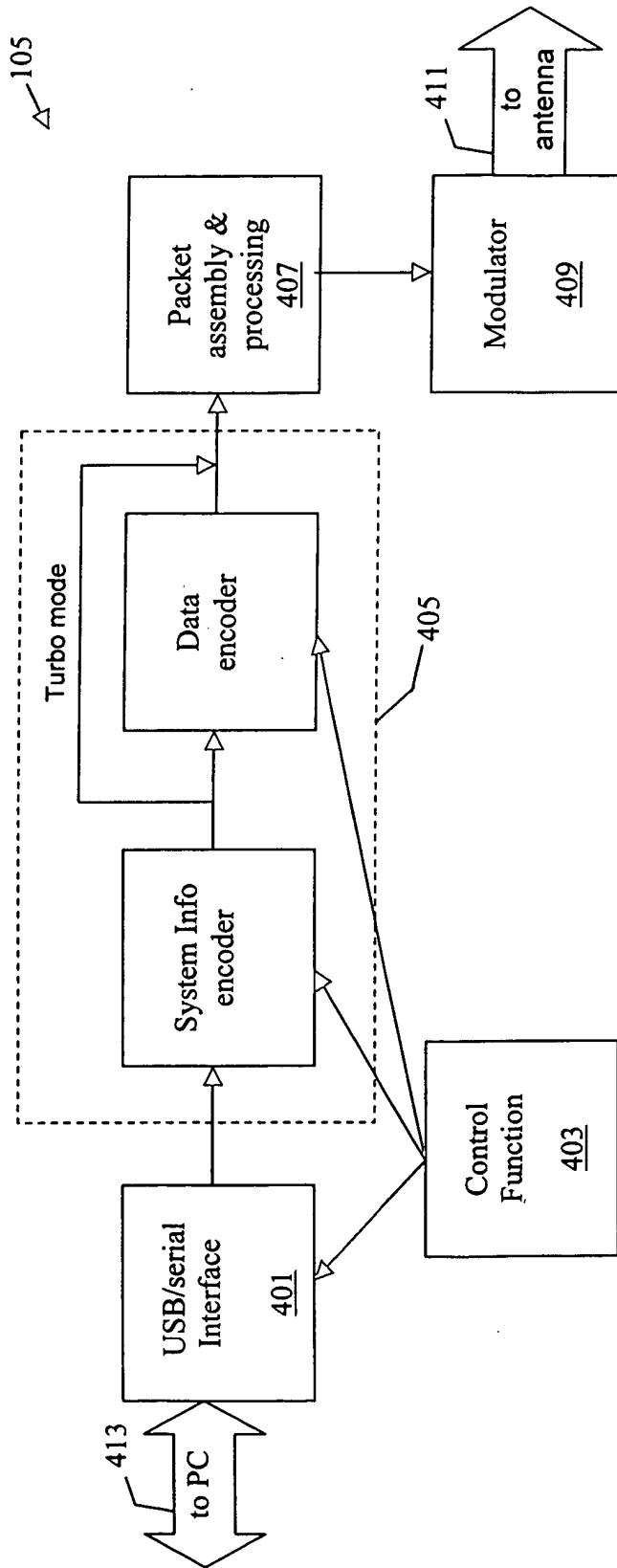


Fig. 4



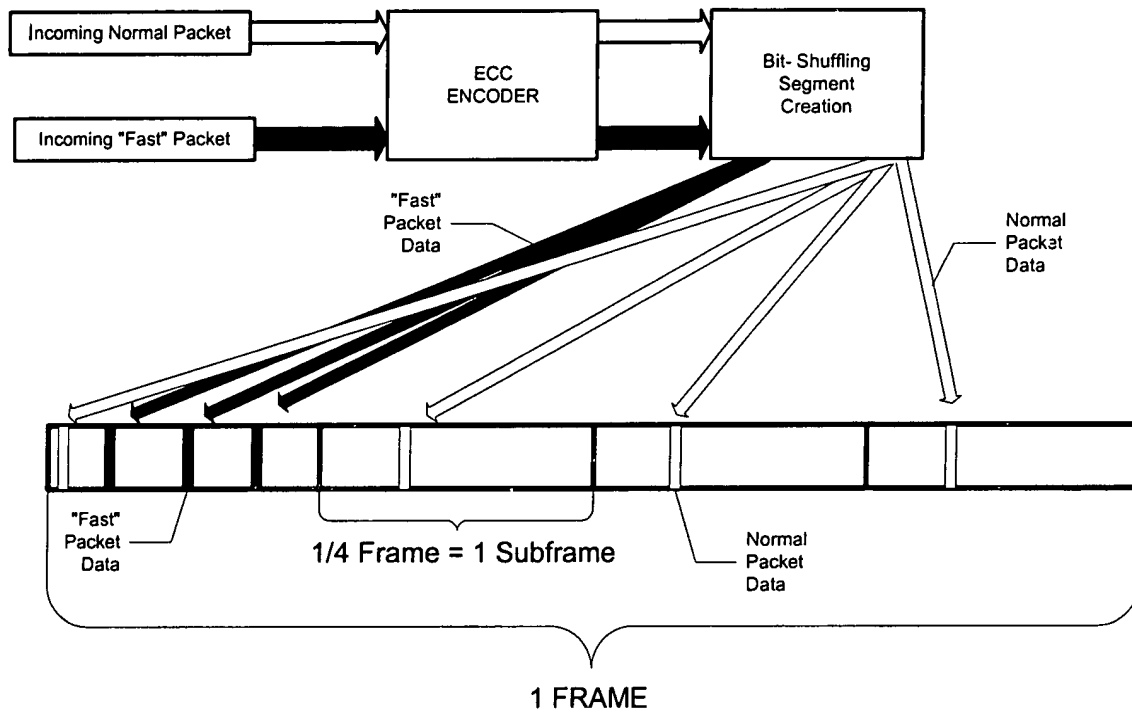
The diagram illustrates the structure of a packet. It consists of the following fields and their sizes:

- FLAG**: 1 Byte
- ADDRESS**: 4 Bytes
- CONTROL**: 8/16 Bytes
- DATA PAYLOAD**: 128-byte packet plus 4-byte header
- CRC BYTES**: 2 Bytes
- FLAG**: 1 Byte

Below the packet structure, a table shows the mapping of fields to specific address ranges:

First	Last
Group addr Hi	Group addr Lo
Generator addr Hi	Generator addr Lo

**Fig. 6**



**Fig. 7**

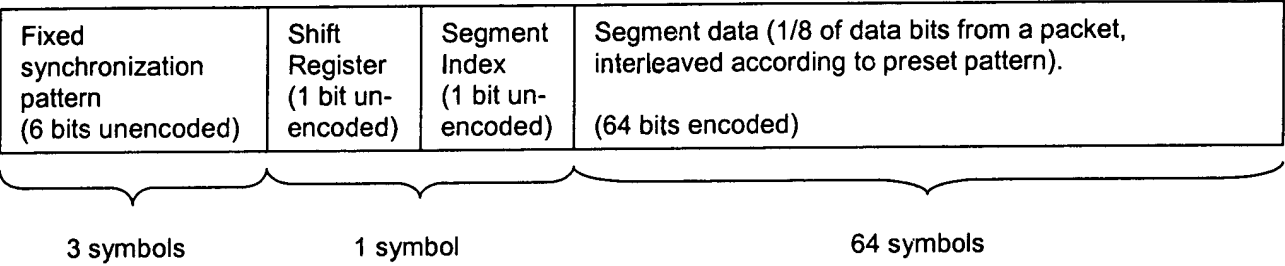


Fig. 8



FIG. 9

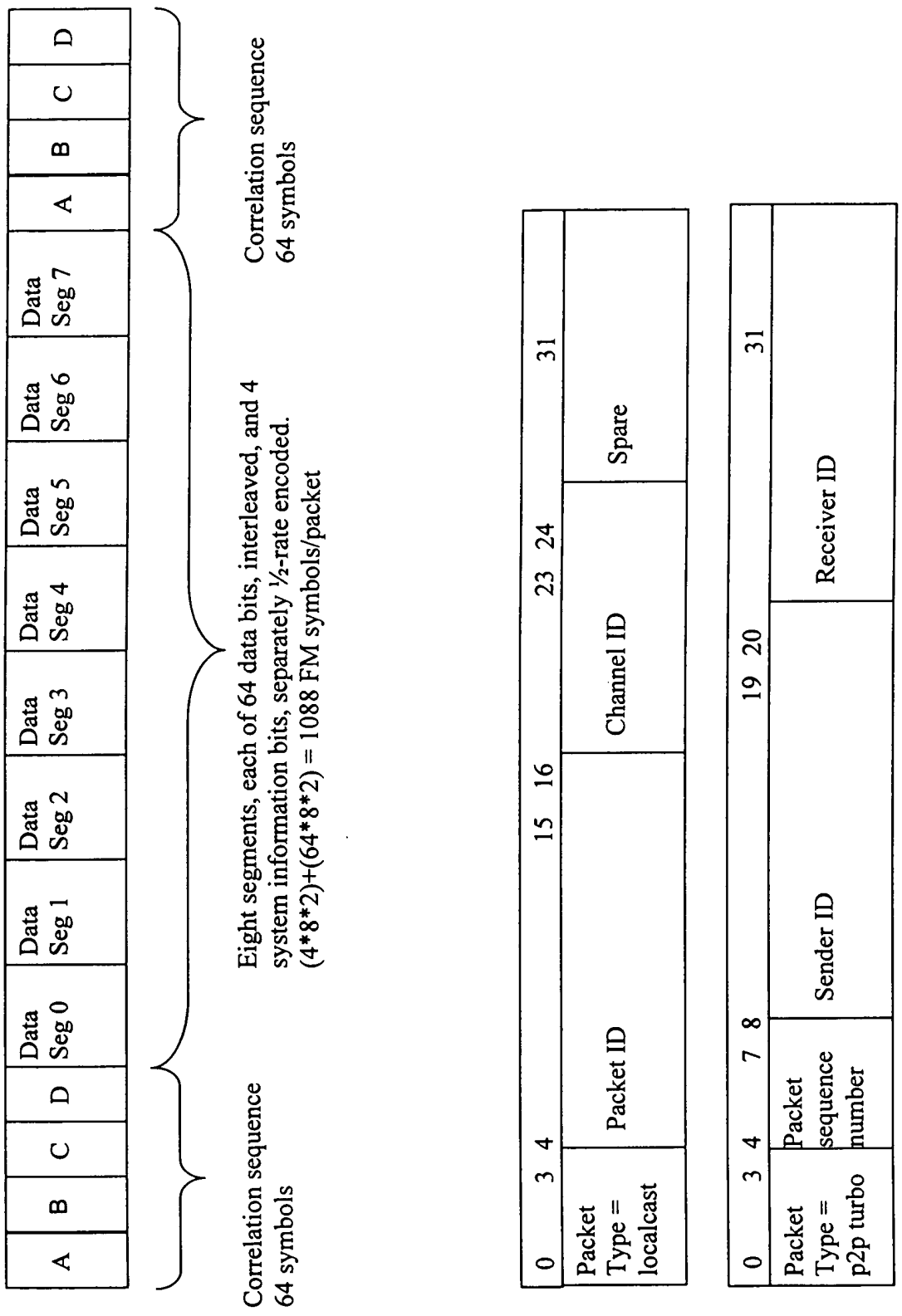
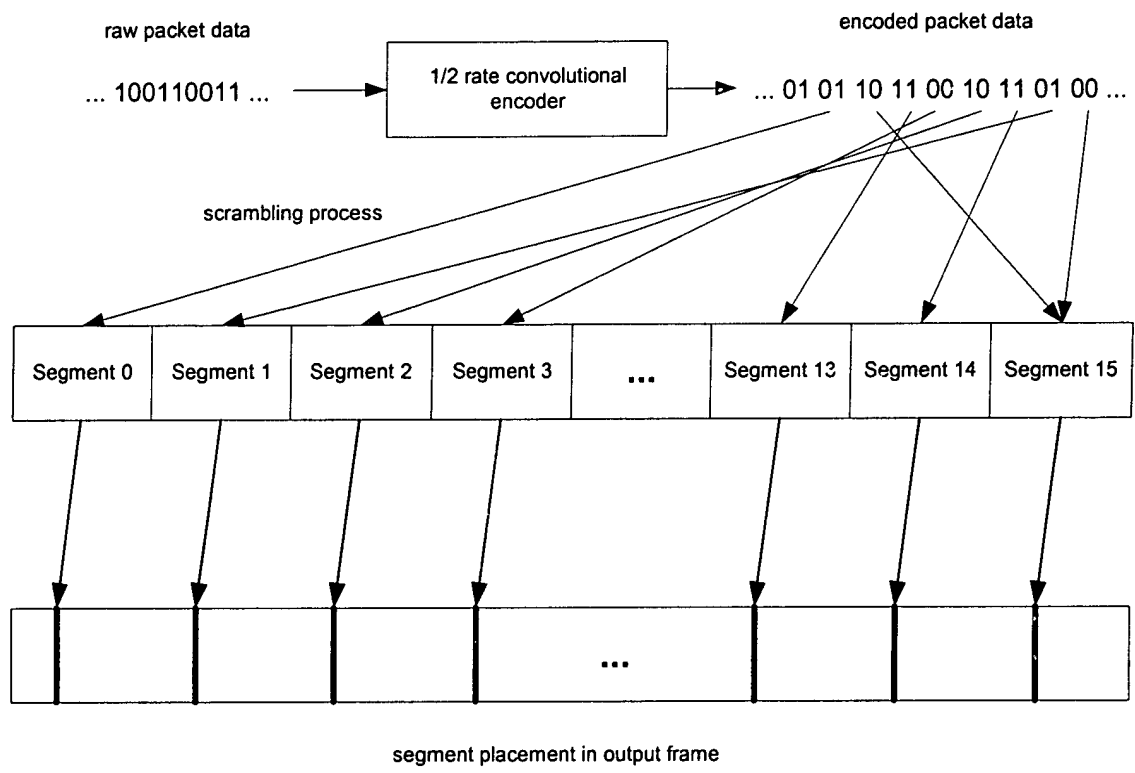
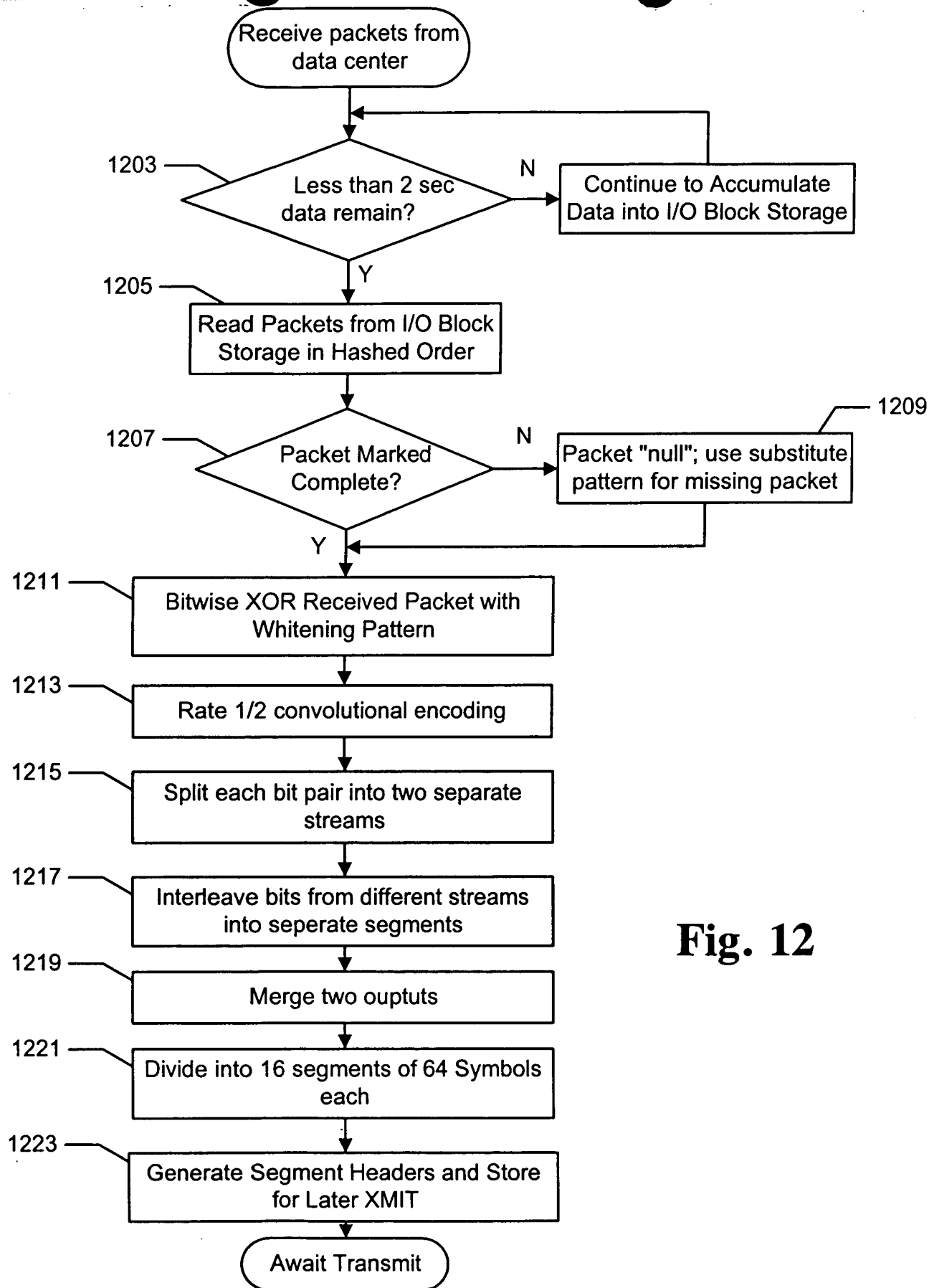


Fig. 9



**Fig. 10**

**Fig. 11**



**Fig. 12**